

L Number	Hits	Search Text	DB	Time stamp
1	4	((("6453407") or ("6249608"))).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 10:21
64	2	((("6601160") or ("6604169"))).PN.	USPAT	2004/04/16 11:25
65	110	("3781810" "4398244" "4472788" "4481576" "4488252" "4511990" "4556938" "4626988" "4730248" "4742479" "4782457" "4800524" "4807172" "4829420" "4829460" "4839846" "4872128" "4882701" "4941120" "4943940" "4959776" "4977533" "4984213" "5007020" "5012441" "5032986" "5038310" "5056004" "5099445" "5101484" "5117498" "5122981" "5155823" "5197023" "5197140" "5206940" "5212662" "5276634" "5282153" "5327543" "5327566" "5379240" "5448703" "5448706" "5463749" "5469377" "5471600" "5497340" "5499380" "5548544" "5568412" "5596760" "5600813" "5619711" "5642516" "5649146" "5659700" "5689693" "5694350" "5696711" "5706460" "5715470" "5737570" "5740419" "5748516" "5764555" "5765218" "5774711" "5778416" "5790443" "5808926" "5812439" "5825730" "5826096" "5828875" "5862065" "5880984" "5892697" "5892699" "5894428" "5909385" "5917741" "5918252" "5930159" "5930503" "5938759" "5941940" "5943249" "5951627" "5951679" "5983333" "5991787" "5996067" "6009454" "6014723" "6026489" "6044392" "6044434" "6049858" "6058409" "6058410" "6058464" "6061780" "6076154" "6101521" "6115732" "6128728" "6134574" "6145049" "6397318").PN.	USPAT USPAT	2004/04/16 11:27

	U	1	Document ID	Issue	Date	Pages	
11			EP 851383 A	19980701	51		Template matching method using similarity evaluation measure
12			JP 10150370	19980602			Viterbi decoder for e.g. opt has calculator that squares

Details Text Image HTML FULL

DERWENT-ACC-NO: 1998-335661

DERWENT-WEEK: 200166

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TITLE: Template matching method using threshold value as similarity evaluation measure - obtaining square of difference between image data value of pixel in subimage to be processed with corresponding pixel in template image, performing cumulative addition on square, and if threshold is exceeded evaluation is closed

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PRIORITY-DATA: 1997JP-0005399 (January 16, 1997), 1996JP-0344955 (December 25, 1996)

PATENT-FAMILY:

PUB-NO	PAGES	MAIN-IPC	PUB-DATE	LANGUAGE
EP 851383 A2	053	G06K 009/64	July 1, 1998	E
US 20010031086 A1	000	G06K 009/62	October 18, 2001	N/A
JP 10187967 A	016	G06T 007/00	July 21, 1998	N/A
JP 10208035 A	017	G06T 005/20	August 7, 1998	N/A

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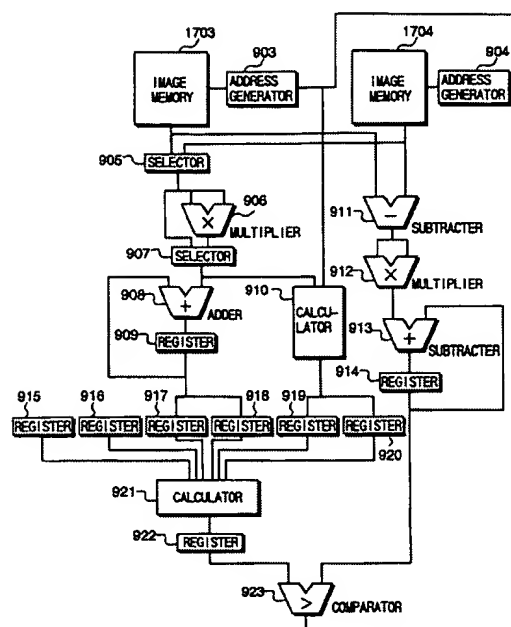
U.S. Patent

Jun. 19, 2001

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US 6,249,608 B1

FIG. 9



U	1	Document ID	Issue Date	Pages	Title
10		US 6453407 B1	20020917	17	Configurable long instruction set
11		US 6401232 B1	20020604	11	Integrated structure layout interconnections for an inst

US-PAT-NO: 6453407

DOCUMENT-IDENTIFIER: US 6453407 B1

TITLE: Configurable long instruction word architecture and instruction set

----- KWIC -----

Detailed Description Text - DETX (17):

Thus, information necessary to fully execute an instruction is divided between the entry in CLIW array 70 (FIG. 3) and the coding of the instruction in program memory 30 as follows: The instruction in program memory 30 contains CLIW array pointer 72 (FIG. 4) which indicates the entry in CLIW array 70 that has the operations template to be executed (equivalent to a decoded VLIW instruction, as previously loaded from program memory 30 by the compiled program). The instruction in program memory 30 also contains information about the external operands, as needed for data fetch stage 26. The CLIW array itself contains information about the operation to be performed (such as add, multiply, square, subtract, etc.) and additional operands which need not be specified until execution stage 28 (such as accumulators, special registers, etc.). When instruction decoders 40 encounter a CLIW instruction (fetched from regular program memory 30), the address of the entry is issued to CLIW array 70 for reading the controls to execution unit 60. The additional data stored in program memory 30 is also decoded (such as memory operands which need to be fetched, and additional information concerning execution unit 60 which has been stored there to reduce total width W of CLIW array 70).

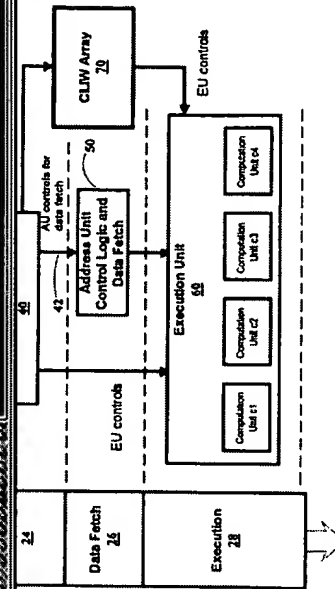


FIG. 3.